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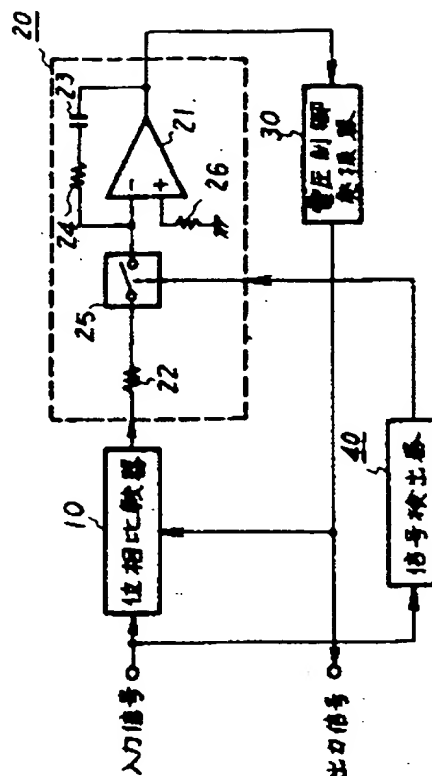
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APPLICANT : NEC CORP;

INVENTOR : OGASAWARA YASUO;

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TITLE : PHASE SYNCHRONISING CIRCUIT



ABSTRACT : PURPOSE: To minimize the variation of output frequency of a voltage control oscillator when an input signal is missed, by adding a simple analog switch and a signal detector to a PLL consisting of a phase comparator, a voltage controlling oscillator and a signal detector.

CONSTITUTION: The input terminal of an operational amplifier 21 of a loop filter 20 is connected to a terminal of an analog switch 25. At the same time, the other terminal of the switch 25 is connected to the output terminal of a phase comparator 10 via an input resistance 22. The output terminal of the amplifier 21 is connected to the input terminal of a voltage controlling oscillator 30. Then the output terminal of a signal detector 40 is connected to the control terminal of the switch 25. When the input signal of a PLL is interrupted, the switch 25 is turned off to hold the electric charge of a capacitor 23. Thus it is possible to keep the output potential of the filter 20, i.e., the control voltage of the oscillator 30.

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